## **PATENT**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

To be assigned

Filing Date:

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Applicants:

Inao Toyoda et al.

Group Art Unit:

To be assigned

Examiner:

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Title:

Photo Sensing Integrated Circuit Device And Related Circuit

Adjustment

Attorney Docket:

4041P-000005/DVA

Director of the United States Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

## PRELIMINARY AMENDMENT

Sir:

Prior to examination of the present application, please consider the following.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.